

Notice of References Cited

Application/Control No.

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Applicant(s)/Patent Under

Reexamination

ROTONDARO, ANTONIO L. P.

Examiner

Michelle Estrada

Art Unit

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Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,348,367 B1	02-2002	Ohtani et al.	438/151
	B	US-6,240,610 B1	06-2001	Ishihara et al.	29/25.01
	C	US-6,297,172 B1	10-2001	Kashiwagi, Akihide	438/773
	D	US-6,083,849 A	07-2000	Ping et al.	134/3
	E	US-6,287,988 B1	09-2001	Nagamine et al.	438/765
	F	US-6,306,774 B1	10-2001	Ping et al.	134/2
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 06097101 A	04-1994	Japan	Kusunoki et al.	H01L 21/20
	O	KR 2001003691 A	01-2001	Korea	Kwak	H01L 21/334
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Stanley et al., "Silicon Processing for the VLSI Era", Vol. 1, 1986 by Lattice Press, pages 215-216.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.